

09686895-101200

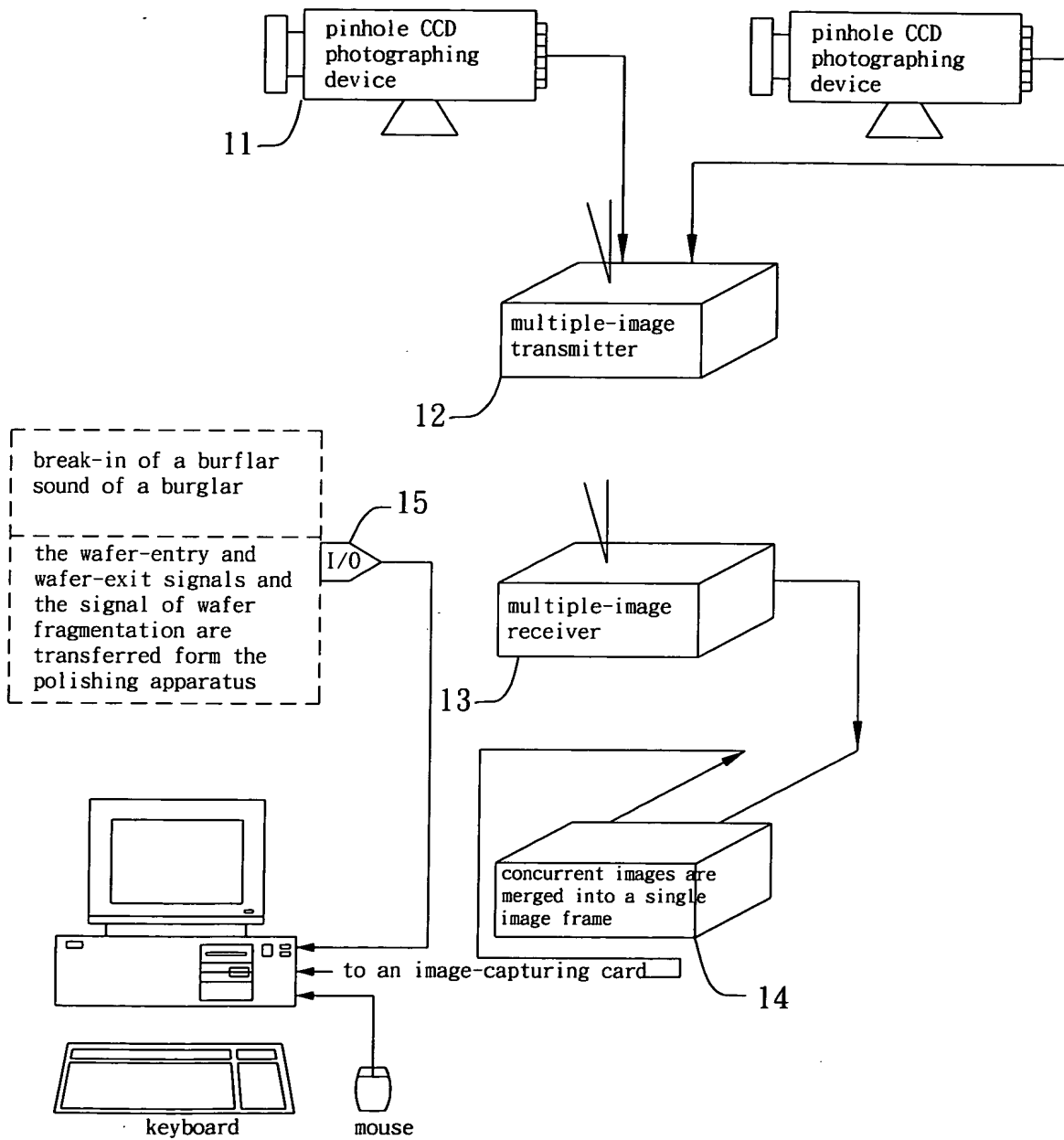


Fig. 1

the wafer-entry and
wafer-exit signals of
wafer fragmentation form
the polishing apparatus

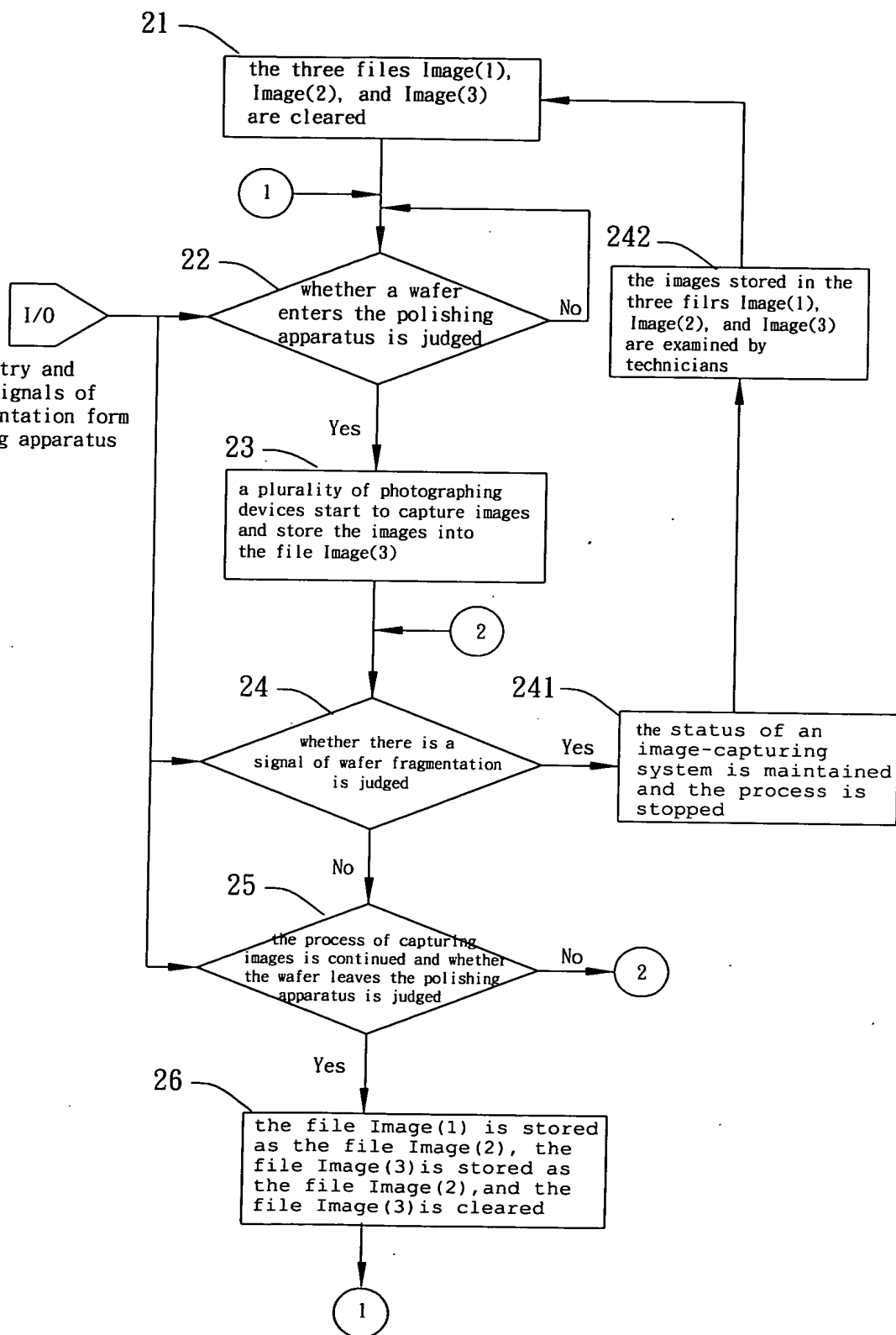


Fig. 2

00210T" 56898950

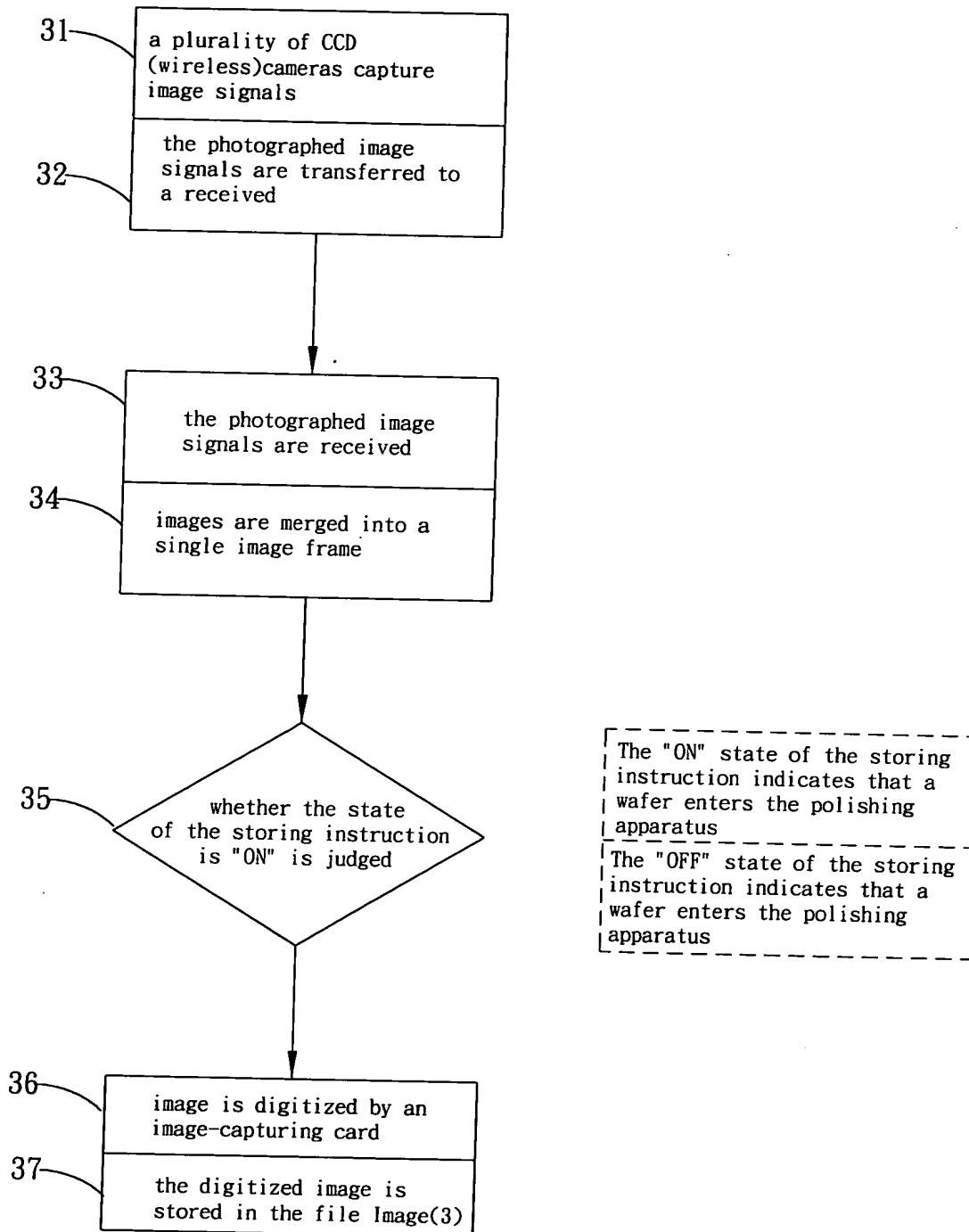


Fig. 3

```
graph TD
    31[31 a plurality of CCD (wireless) cameras capture image signals] --> 32[32 the photographed image signals are transferred to a received]
    32 --> 33[33 the photographed image signals are received]
    33 --> 36[36 image is digitized by an image-capturing card]
    36 --> 34[34 images are merged into a single image frame]
    34 --> 35{35 whether the state of the storing instruction is "ON" is judged}
    35 -- Yes --> 37[37 the digitized image is stored in the file Image(3)]
    35 -- No --> 38[38 functional circuits are combined to form a control card]
    38 --> 35
    35 --> 39[39 The "ON" state of the storing instruction indicates that a wafer enters the polishing apparatus]
    35 --> 40[40 The "OFF" state of the storing instruction indicates that a wafer enters the polishing apparatus]
    39 --> 35
    40 --> 35
    37 --> 41[41 processed by a PC]
    41 --> 37
```

31 a plurality of CCD (wireless) cameras capture image signals

32 the photographed image signals are transferred to a received

33 the photographed image signals are received

36 image is digitized by an image-capturing card

34 images are merged into a single image frame

35 whether the state of the storing instruction is "ON" is judged

37 the digitized image is stored in the file Image(3)

functional circuits are combined to form a control card

The "ON" state of the storing instruction indicates that a wafer enters the polishing apparatus

The "OFF" state of the storing instruction indicates that a wafer enters the polishing apparatus

processed by a PC

Fig. 4

00210T" 56898960

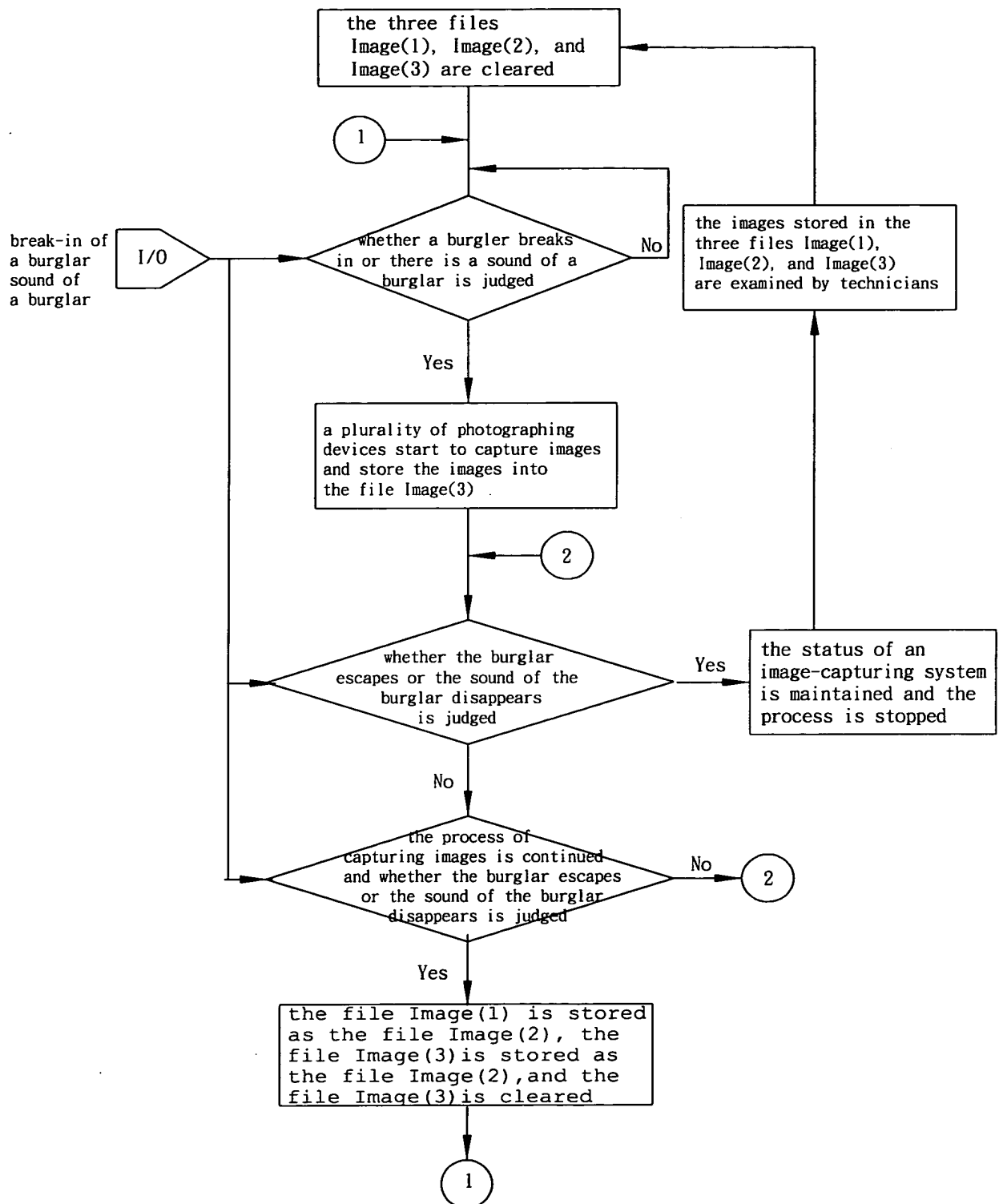


Fig. 5